

09/26/50

**UNITED STATES PATENT AND TRADEMARK  
OFFICE  
REQUEST FOR FILING  
(RULE 53(b)(1))**

### For Design or Utility Applications

Rule 53(b)(1) PATENT APPLICATION:

**(DO NOT USE FOR CIPs)**

☒ Continuation )  
 ) application under 37 CFR 1.53(b)(1)  
☐ Divisional )  
 application under 37 CFR 1.53(b)(1)  
 of pending prior application of

Group Art Unit: Unknown

Examiner: Unknown

Inventor(s): ELLIS, III

Parent Appln. No.:	09	320,660
	Series Code ↑	Serial No. ↑

Atty. Dkt.	PM 265678	GNC7-CON1
	New M#	Client Ref

Parent Filed: May 27, 1999

This Appln. Filed: September 26, 2000

Title: GLOBAL NETWORK COMPUTERS

Hon. Commissioner of Patents  
Washington, DC 20231

Date: September 26, 2000  
(Parent Matter No. 251017)

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To effect the above-requested filing today:

**Attached** is a copy (which must be filed) of the prior application, including:

- ☐ Abstract
- ☒ Specification and claims (54 pages) (**must be attached**)
- ☒ Drawings (**must be attached if originally filed**): 6 sheet(s)/set: ☐ 1 set informal;  
☒ Formal of size ☒ A4 ☐ 11"

1A. Always X one box, only:

- (1) ☒ Original Declaration attached.
- (2) ☐ NO declaration or fee is enclosed; therefore, this is a filing under Rule 53(f).

2. ☐ This application is hereby filed by less than all of the inventors named in the prior application. Petition is hereby made requesting deletion as inventor(s) of the following who is/are **not** inventor(s) of the invention being claimed in this application (DELETE THE FOLLOWING INVENTOR(S)):

1. \_\_\_\_\_
3. \_\_\_\_\_
5. \_\_\_\_\_
7. \_\_\_\_\_
2. \_\_\_\_\_
4. \_\_\_\_\_
6. \_\_\_\_\_
8. \_\_\_\_\_

2.5 THE INVENTOR(S) FOR THIS NEW APPLICATION IS(ARE):

1. \_\_\_\_\_
3. \_\_\_\_\_
5. \_\_\_\_\_
7. \_\_\_\_\_
2. \_\_\_\_\_
4. \_\_\_\_\_
6. \_\_\_\_\_
8. \_\_\_\_\_

- 3. The entire disclosure of the prior application is considered as being part of the disclosure of the accompanying application and is hereby incorporated therein by reference thereto.

4. ☐ Priority is claimed under 35 U.S.C. 119/365 based on filing in \_\_\_\_\_ Of \_\_\_\_\_ (country)

	<u>Application No.</u>	<u>Filing Date</u>		<u>Application No.</u>	<u>Filing Date</u>
(1)	_____	_____	(2)	_____	_____
(3)	_____	_____	(4)	_____	_____
(5)	_____	_____	(6)	_____	_____

a. ☐ \_\_\_\_\_ (No.) Certified copy/copies attached.

b. ☐ Certified copy/copies previously filed on \_\_\_\_\_ In \_\_\_\_\_ U.S. Application No. \_\_\_\_\_ / \_\_\_\_\_, filed on \_\_\_\_\_.

c. ☐ Certified copy/copies filed during International stage of PCT/ \_\_\_\_\_ / \_\_\_\_\_ series code ↑ serial no.

4. (a) ☐ Domestic priority is claimed from
- |                 |                    |
|-----------------|--------------------|
| 60/031,855      | filed 29 Nov. 1996 |
| 60/033,871      | 20 Dec. 1996       |
| 60/066,415      | 24 Nov. 1997       |
| 60/032,207      | 02 Dec. 1996       |
| 60/066,313      | 21 Nov. 1997       |
| 08/980,058      | 26 Nov. 1997       |
| PCT/US97/21812, | 28 Nov. 1997       |

(b) ☐ Benefit is claimed of Provisional Application No. 60/\_\_\_\_, filed \_\_\_\_.

5. ☐ Prior application is assigned to \_\_\_\_\_

by assignment recorded \_\_\_\_\_ Reel \_\_\_\_\_ Frame \_\_\_\_\_ (Date)

6. ☐ Attached is the following number of Assignments (including original and all later successive ones by Different \_\_\_\_\_ and respective **new** Cover Sheets. (Do **NOT** file old cover sheets.) assigners): \_\_\_\_\_

(Assignments in parent **must be refiled** with new Cover Sheets in this continuing application if you want it/them recorded against the continuing application.)

Please return the recorded Assignment to the undersigned.

☐ The power of attorney in the prior application is to \_\_\_\_\_

(Name and Reg. No.)  
whose current address is as in item 8 below.

a. ☐ Recognize as associate attorney \_\_\_\_\_

(Name, Reg. No. and Address)

8. **Address all future communications to Intellectual Property Group of Pillsbury Madison & Sutro LLP, Ninth Floor, East Tower 1100 New York Avenue, N.W., Washington, D.C. 20005-3918**

9. ☒ **Amend the specification** by inserting before the first line the sentence:--This is a ☒ continuation ☐ division of Application No. 09/320,660, filed May 27, 1999

series code ↑ serial no.  
International Appln. No. PCT/US97/21812 filed November 28, 1997 which was a CIP of 08/980,058 filed November 26, 1997; Application 09/320,660 claims the benefit of U.S. Provisional Applications 60/031,855 filed November 29, 1996; 60/032,207 filed December 2, 1996; 60/033,871 filed December 20, 1996; 60/066,313 filed November 21, 1997; and 60/066,415 filed November 24, 1997.--

9. (a) ☐ **Amend the specification** by inserting before the first line: --This application claims the benefit of Provisional Application No. 60/\_\_\_\_, filed \_\_\_\_ --

10. ☒ It has been recently determined that this new continuing application is entitled to small entity status.

Hence:

1 (No.) Verified Statement(s) establishing "small entity" status under Rules 9 & 27 were/are:

- ☐ filed in above prior application (and hence applicable hereto)  
☒ attached.

11. Petition to extend the life of the above prior application to at least the date hereof

(one box) ☐ is being concurrently filed in that prior application (Use Form PAT-111).

(must be) ☐ was previously filed in that prior application (Check length of prior extension).

(X'd) ☒ is not necessary for copendency (**Double check** before X'ing this box).

12. ☒ **INFORMATION DISCLOSURE STATEMENT:** Attached is Form PTO-1449 listing all of the documents cited by Applicant and the PTO in the parent application(s) relied upon under 35 USC 120 and referenced in item 9 above. Per Rule 98(d) copies of those documents are not required now. Please consider those documents and advise that they have been considered in this new application as by returning a copy of the enclosed Form PTO-1449 with the Examiner's initials in the left column per MPEP 609. .

13. ☐ Attached is a Rule 103(a) Petition to Suspend Action.

14. ☒ **PRELIMINARY AMENDMENT to be entered before fee calculation:** (Do not make amendments here except for correction of improper multiple dependencies or cancellation of whole claims or multiple dependencies for purpose of reducing the filing fee per MPEP §§ 506 and 607; do not cancel all claims).

Cancel claims 2-51.

#### FILING FEE

THE FOLLOWING FILING FEE IS BASED ON

-->-->--> CLAIMS AS FILED AND CHANGED BY PRELIMINARY AMENDMENT IN ITEM 14 <--<--<--<

**NOTE:** If box 1A2 is X'd, do not pay fees,  
but leave lines 15-22 and 27-32 blank.

**PTO: PLEASE NOTE CLAIM CANCELLATIONS IF BOX 14 ABOVE IS X'D.**

				Large/Small Entity		Fee Code
15. Basic Filing Fee ..... Design Application				\$310/\$155		106/26
16. Basic Filing Fee ..... Not Design Application				\$690/\$345	+345	101/201
17. Total Effective Claims	1	minus 20 =	0	x \$18/\$9	+0	103/203
18. Independent Claims	1	minus 3 =	0	x \$78/\$39	+0	102/202
19. If <u>any proper</u> multiple dependent claim (ignore improper) is present,				\$260/\$130	+0	104/204
20. Subtotal =				\$0		
21. If "petition" box 13 above is X'd, add petition fee. .... \$130					+0	122
21A. If box 6 above is X'd, add Assignment recording fee ..... \$ 40					+0	581
22. TOTAL FILING FEE ATTACHED =					\$345	

(carry forward to Item 31)

23. ☐ ATTACHED:

24. ☒ Preliminary Amendment attached (to be entered after assigning Appln. No.)

25. ☐ The following PRELIMINARY AMENDMENT is to be entered after assigning Appln. No.:

26.

**ADDITIONAL FEE CALCULATION FOR  
PRELIMINARY AMENDMENT  
PER BOXES 24/25**

	Claims remaining after amendment	Highest number previously paid for	Present Extra	Additional Fee	
					<u>Large/Small Entity</u> <u>File Code</u>
27.	Total Effective Claims <u>66</u>	minus ** <u>20</u>	= <u>46</u> x \$18/\$9	= \$ <u>414</u>	(103/203)
28.	Independent Claims <u>4</u>	minus *** <u>3</u>	= <u>1</u> x \$78/\$39	= + <u>39</u>	(102/202)
29.	If amendment enters proper multiple dependent claim(s) into this application for the <u>first time</u> , add (per application) .....			\$260/\$130 + <u>0</u>	(104/204)
30.	ADDITIONAL FEE			\$ <u>453</u>	
31.	plus FEE from item 22 on page 3			+ <u>345</u>	
32.	<b><u>TOTAL FEE ATTACHED</u></b>			\$ <u><u>798</u></u>	

33. \*If the entry in this space is less than a entry in the next space, the "Present Extra" result is "0"

34. \*\*If the "Highest number previously paid for" (see item 17 above) is less than 20, write "20" in this space

35. If the "Highest number previously paid for" (see item 18 above) is less than 3, write "3" in this space

Our Deposit Account No. 03-3975

Our Order No. <u>81498</u>	<u>265678</u>
C#	M#

**CHARGE STATEMENT:** Upon the filing of a Declaration pursuant to Rule 60(b) or 60(d), the Commissioner is hereby authorized to charge any fee specifically authorized hereafter, or any missing or insufficient fee(s) filed, or asserted to be filed, or which should have been filed herewith or concerning any paper filed hereafter, and which may be required under Rules 16-18 (missing or insufficient fee only) now or hereafter relative to this application and the resulting Official document under Rule 20, or credit any overpayment, to our Account/Order Nos. shown above for which purpose a duplicate copy of this sheet is attached.

**This CHARGE STATEMENT does not authorize charge of the issue fee until/unless an issue fee transmittal form is filed.**

**Pillsbury Madison & Sutro LLP  
Intellectual Property Group**

1100 New York Avenue, NW  
Ninth Floor  
Washington, DC 20005-3918  
Tel: (202) 861-3000  
RCI/ksh  
Atty./Sec.

By Atty: Richard C. Irving

Reg. No. 38499

Sig: *Richard C. Irving*

Fax: (202) 822-0944

Tel: (202) 861-3788

**NOTE No. 1:** File this Request in duplicate with 2 postcard receipts (PAT-103) & attachments

**NOTE No. 2:** Is extension in parent necessary for copendency? DOUBLE CHECK item 11 above.

If yes, printout Pat-111 and head it in parent.

Inventor(s): Frampton E. ELLIS  
Appln. /                      or Patent No.:                     

Atty. Dkt.  
PMS 265678/GNC7-CON1  
M# / Client Ref.

Filed: September 25, 2000 or Issued:                     

For: GLOBAL NETWORK COMPUTERS

VERIFIED STATEMENT (DECLARATION) CLAIMING SMALL ENTITY  
STATUS (37 CFR 1.9(f) and 1.27 (b)) - **INDEPENDENT INVENTOR**

As a below named inventor, I hereby declare that I qualify as an independent inventor as defined in 37 CFR 1.9(c) for purposes of paying reduced fees under Section 41(a) and (b) of Title 35, United States Code, to the Patent and Trademark Office with regard to the invention entitled as above and described in

X ☒ the specification filed herewith  
one → ☐ Application No.       , filed  
box → ☐ Patent No.       , issued

I have not assigned, granted, conveyed or licensed and am under no obligation under contract or law to assign, convey or license any rights in the invention to any person who could not be classified as an independent inventor under 37 CFR 1.9(c) if that person had made the invention, or to any concern which would not qualify as a small business concern under 37 CFR 1.9(d) or a nonprofit organization under 37 CFR 1.9(e):

Each (small entity) person, concern or organization to which I have assigned, granted, conveyed, or licensed or am under an obligation under contract or law to assign, grant, convey, or license any rights in the invention

X → ☒ there is no such person, concern, or organization.  
one → ☐ such persons, concerns or organizations are listed in (A) and (B) below:


(A) FULL NAME of assignee/licensee/grantee/conveyee\*  
ADDRESS  
☐ INDIVIDUAL ☐ SMALL BUSINESS CONCERN ☐ NONPROFIT ORGANIZATION

(B) FULL NAME of assignee/licensee/grantee/conveyee\*  
ADDRESS  
☐ INDIVIDUAL ☐ SMALL BUSINESS CONCERN ☐ NONPROFIT ORGANIZATION

\*NOTE: Separate verified statement is required from each person, concern or organization named in (A) and (B) above having rights to the invention, averring to his/her/its status as a small entity. (37 CFR 1.27)

I acknowledge the duty to file, in this case, notification of any change in status resulting in loss of entitlement to small entity status prior to paying, or at the time of paying, the earliest of the issue fee or any maintenance fee due after the date on which status as a small entity is no longer appropriate. (37 CFR 1.28(b))

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this verified statement is directed.

1. <u>Frampton E. ELLIS</u> NAME OF INVENTOR  Signature of Inventor <u>9/25/00</u> Date	2. <u>                                    </u> NAME OF INVENTOR <u>                                    </u> Signature of Inventor <u>                                    </u> Date	3. <u>                                    </u> NAME OF INVENTOR <u>                                    </u> Signature of Inventor <u>                                    </u> Date
---	---	---

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re PATENT APPLICATION of

**ELLIS, III**

Group Art Unit: **Unknown**

Appln. No.: **Continuation of  
Application 09/320,660**

Examiner: **Unknown**

Filed: **September 26, 2000**

FOR: **GLOBAL NETWORK COMPUTERS**

\* \* \* \* \*

**September 26, 2000**

**PRELIMINARY AMENDMENT**

Hon. Commissioner of Patents  
and Trademarks  
Washington, D.C. 20231

Sir:

Kindly enter the following preliminary amendment.

**IN THE TITLE OF THE INVENTION:**

Kindly replace the Title of the Invention with --Internal Personal Computer Firewall  
for Complete Denial of Access by Network to User's Secure Portion--.

**IN THE CLAIMS:**

Kindly cancel claim 1 without prejudice and add the following new claims:

--52. An apparatus, comprising:

a firewall configured to operate in a personal computer, which is configured to operate

with other computers connected in a network;

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said personal computer including at least two microprocessors;

said firewall configured to deny access to at least a first microprocessor of said personal computer by at least one of said other computers of said network during a shared operation involving said personal computer and said at least one of said other computers of said network; and

said firewall configured to allow access to at least a second microprocessor of said personal computer by said at least one of said other computers of said network during said shared operation.

53. The apparatus of claim 52, wherein:

said firewall is configured to deny access to at least a first microprocessor of said personal computer by said other computers of said network during a shared operation involving said personal computer and at least one of said other computers of said network.

54. The apparatus of claim 53, wherein:

said firewall is configured to allow access to at least a second microprocessor of said personal computer by said other computers of said network during said shared operation.

55. An apparatus, comprising:

a firewall configured to operate in a personal computer, which is configured to operate with other computers connected in a network;

said personal computer including at least two microprocessors and at least two

memory hardware components;

said firewall configured to deny access to at least a first microprocessor and at least a first memory hardware component of said personal computer by at least one of said other computers during a shared operation involving said personal computer and said at least one of said other computers of said network; and

said firewall configured to allow access to at least a second microprocessor and at least a second memory hardware component of said personal computer by said at least one of said other computers of said network during said shared operation.

56. The apparatus of claim 55, wherein:

said firewall configured to deny access to at least a first microprocessor and at least a first memory hardware component of said personal computer by said other computers of said network during a shared operation involving said personal computer and at least one of said other computers of said network.

57. The apparatus of claim 56, wherein:

said firewall is configured to allow access to at least a second microprocessor and at least a second memory hardware component of said personal computer by said other computers of said network during said shared operation.

58. An apparatus, comprising:

a firewall configured to operate in a personal computer, which is configured to operate



with other computers connected in a network;

said personal computer including at least one microprocessor and at least two memory hardware components;

said firewall configured to deny access to at least a first memory hardware component of said personal computer by at least one of said other computers during a shared operation involving said personal computer and said at least one of said other computers of said network; and

said firewall configured to allow access to at least a second memory hardware component of said personal computer by said at least one of said other computers of said network during said shared operation.

59. The apparatus of claim 58, wherein:

said firewall is configured to deny access to at least a first memory hardware component of said personal computer by said other computers of said network during a shared operation involving said personal computer and at least one of said other computers of said network.

60. The apparatus of claim 59, wherein:

said firewall is configured to allow access to at least a second memory hardware component of said personal computer by said other computers of said network during said shared operation.

61. The apparatus of claim 59, wherein:

said firewall is configured to deny access to at least said second memory hardware component of said personal computer by said personal computer during said shared operation.

62. The apparatus of claim 59, wherein said first memory hardware component is accessible only by a user of said personal computer.

63. The apparatus of claim 59, wherein said personal computer microprocessor is controlled by a user of said personal computer through operation of a wireless controller by said user.

64. The apparatus of claim 59, wherein said access to said personal computer by said other computers of said network is controlled electromechanically.

65. The apparatus of claim 59, wherein a user of said personal computer preemptively controls access to said personal computer by said other computers of said network through operation of a manually activated switch.

66. The apparatus of claim 59, wherein said manually activated switch is a keyboard of said personal computer.

67. The apparatus of claim 59, wherein said firewall includes a hardware component.

68. The apparatus of claim 59, wherein said firewall includes a software component.

69. The apparatus of claim 59, wherein said firewall includes a firmware component.

70. The apparatus of claim 59, wherein said shared operation is initiated by a user of said personal computer.

71. The apparatus of claim 59, wherein said shared operation is initiated by said at least one said other computer.

72. The apparatus of claim 59, wherein said access is allowed to at least said second memory hardware component of said personal computer when said personal computer is idled by a user of said personal computer.

73. The apparatus of claim 59, wherein said firewall denies access at least temporarily to a microprocessor of said personal computer by at least one said other computer during said shared operation.

74. The apparatus of claim 59, wherein said firewall allows access at least temporarily to a microprocessor of said personal computer by at least one said other computer during said shared operation.

75. The apparatus of claim 59, wherein said first memory hardware component is a hard drive device.

76. The apparatus of claim 59, wherein said first memory hardware component is a flash memory device.

77. The apparatus of claim 59, wherein said first memory hardware component is a flash bios.

78. The apparatus of claim 59, wherein said second memory hardware component is a random access memory (RAM) device.

79. The apparatus of claim 59, wherein said second memory hardware component is a hard drive device.

80. The apparatus of claim 59, wherein said second memory hardware component is a read-only compact disk drive (CD-ROM) device.

81. The apparatus of claim 59, wherein said second memory hardware component is a read-only digital video disk drive (DVD) device.

**ELLIS -- Continuation of Appln. No.: 09/320,660**

82. The apparatus of claim 59, wherein said personal computer includes a plurality of microprocessors.

83. The apparatus of claim 59, wherein said personal computer includes a microchip with a plurality of microprocessors.

84. The apparatus of claim 59, wherein said personal computer is substantially contained in a respective single microchip.

85. The apparatus of claim 59, wherein said personal computer is substantially contained in a single respective microchip including a plurality of microprocessors.

86. The apparatus of claim 59, wherein said network of computers includes an Internet.

87. The apparatus of claim 59, wherein said network of computers includes a World Wide Web.

88. The apparatus of claims 59, wherein said personal computer is an appliance with a microprocessor.

89. The apparatus of claim 59, wherein said personal computer includes an optical

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fiber connection to said network.

90. The apparatus of claim 83, wherein said microchip includes an optical fiber connection to said network.

91. The apparatus of claim 84, wherein said microchip includes an optical fiber connection to said network.

92. The apparatus of claim 59, wherein said shared operation is one of parallel processing or multitasking.

93. The apparatus of claim 59, wherein at least one said other computer and said personal computer are connected via a peer-to-peer relationship.

94. The apparatus of claim 59, wherein:  
said personal computer is an appliance with a microprocessor, and  
said appliance includes one of a handheld personal digital assistant, a telephone, a pager, a television, a game, a videotape player/recorder, a video camera, a compact disk (CD) player/recorder, a digital video disk (DVD) player/recorder, a radio, a camera, a printer, a fax machine, and an automobile.

95. The apparatus of claim 59, wherein a user of said personal computer retains

preemptive control of at least said second memory hardware component.

96. The apparatus of claim 59, wherein a user of said personal computer retains preemptive control of all components of said personal computer.

97. The apparatus of claim 59, wherein at least a part of said personal computer is configured to function as a master in said shared operation.

98. The apparatus of claim 59, wherein at least a part of said personal computer is configured to function as a slave in said shared operation.

99. The apparatus of claim 59, wherein said second memory hardware component is volatile memory.

100. The apparatus of claim 59, wherein said first memory hardware component is non-volatile memory.

101. The apparatus of claim 59, wherein said second memory hardware component is a same type of hardware component as said first memory hardware component, said type being one of a flash memory device, a flash bios, a random access memory (RAM), a hard drive device, a read-only compact disk drive (CD-ROM), a read-only compact digital video disk (DVD) device, a volatile memory, a non-volatile memory, a read and write memory, and

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a read-only memory.

- 102. The apparatus of claim 59, wherein said first memory hardware component is read and write memory.

103. The apparatus of claim 59, wherein said second memory hardware component is read-only memory.

104. The apparatus of claim 59, wherein said personal computer is configured to communicate with said network through a connection having a speed of data transmission that is greater than a peak data processing speed of said personal computer.

105. The apparatus of claims 59, wherein at least one microprocessor of said personal computer is configured to communicate with said network through a connection having a speed of data transmission that is greater than a peak data processing speed of said at least one microprocessor.

106. The apparatus of claim 59, wherein said personal computer includes at least four microprocessors.

107. The apparatus of claim 59, wherein said personal computer includes at least eight microprocessors.



108. The apparatus of claim 59, wherein said personal computer includes at least 16 microprocessors.

109. The apparatus of claim 59, wherein said personal computer includes at least 32 microprocessors.

110. The apparatus of claim 59, wherein said personal computer includes at least 64 microprocessors.

111. The apparatus of claim 59, wherein said personal computer includes at least 128 microprocessors.

112. The apparatus of claim 59, wherein said personal computer includes at least 256 microprocessors.

113. The apparatus of claim 59, wherein said personal computer includes at least 512 microprocessors.

114. The apparatus of claim 59, wherein said personal computer includes at least 1024 microprocessors.

**ELLIS -- Continuation of Appln. No.: 09/320,660**

115. The apparatus of claim 59, wherein said another computer and said personal computer are connected via a client/server relationship.

116. A machine-readable medium having information recorded therein such that when said information is read and executed by a processor within a personal computer arranged to operate with other computers connected in a network, said processor is caused to:

perform a firewall function within said personal computer;

said firewall function being configured to deny access to at least a first memory hardware component of said personal computer by at least one of said other computers during a shared operation involving said personal computer and said at least one of said other computers of said network;

said firewall function being configured to allow access to at least a second memory hardware component of said personal computer by said at least one of said other computers of said network during said shared operation.

117 . The machine-readable medium of claim 116, wherein:

said firewall function is configured to deny access to at least a first memory hardware component of said personal computer by said other computers of said network during a shared operation involving said personal computer and at least one of said other computers of said network; and

said firewall function is configured to allow access to at least a second memory hardware component of said personal computer by said at least one of said other computers of

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said network during said shared operation. --

**REMARKS**

Early and favorable action on the merits are respectfully requested.

Respectfully submitted,

PILLSBURY MADISON & SUTRO LLP

By Richard C. Irving  
Richard C. Irving  
Reg. No. 38,499  
Tel. No.: (202) 861-3788  
Fax No.: (202) 822-0944

DSL/RCI  
1100 New York Avenue, N.W.  
Ninth Floor  
Washington, D.C. 20005-3918  
(202) 861-3000

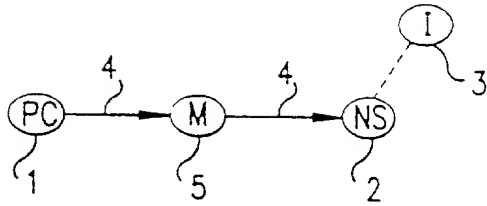


FIG. 1

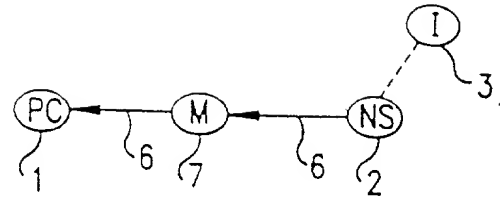


FIG. 2

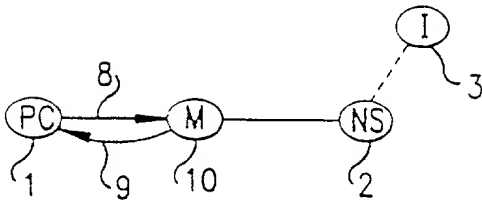


FIG. 3

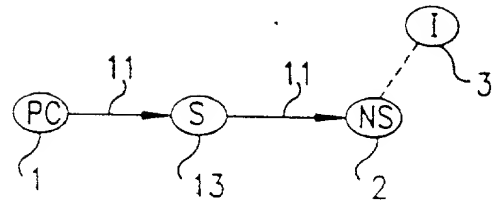


FIG. 4A

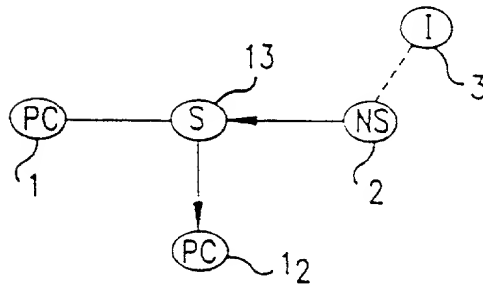


FIG. 4B

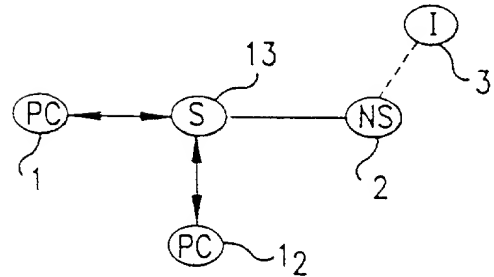


FIG. 4C

009260" 03269960

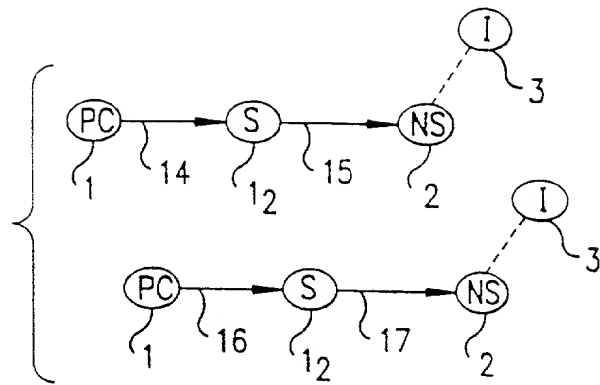


FIG. 5

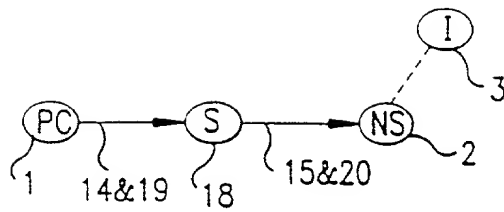


FIG. 6

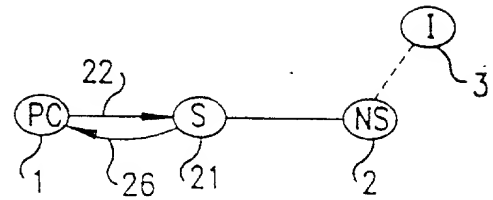


FIG. 7

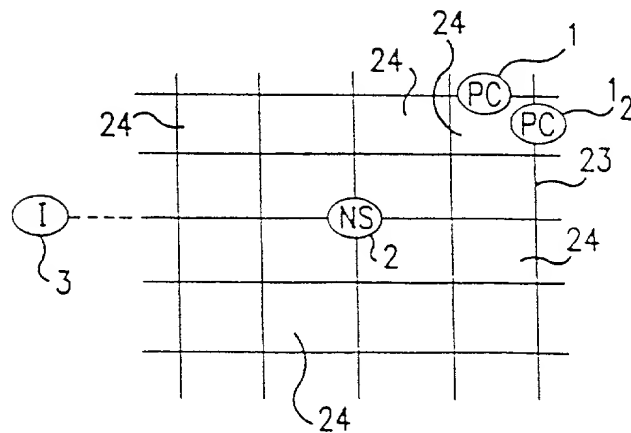


FIG. 8



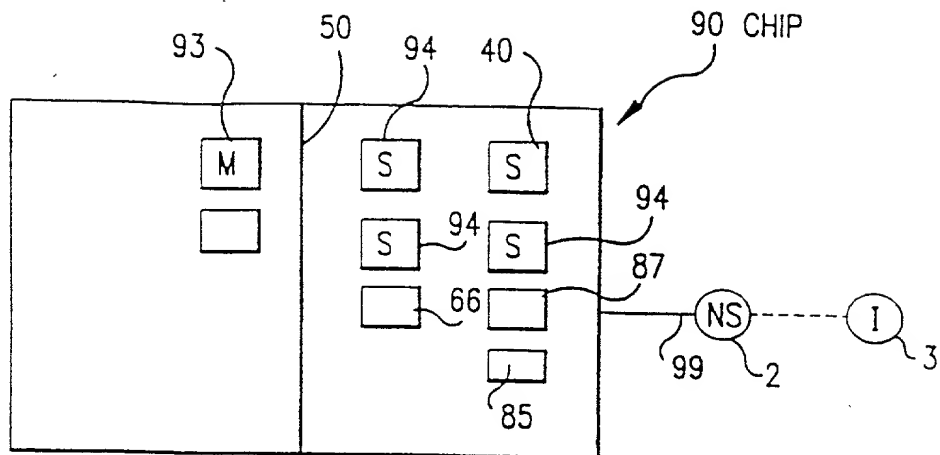


FIG. 10C

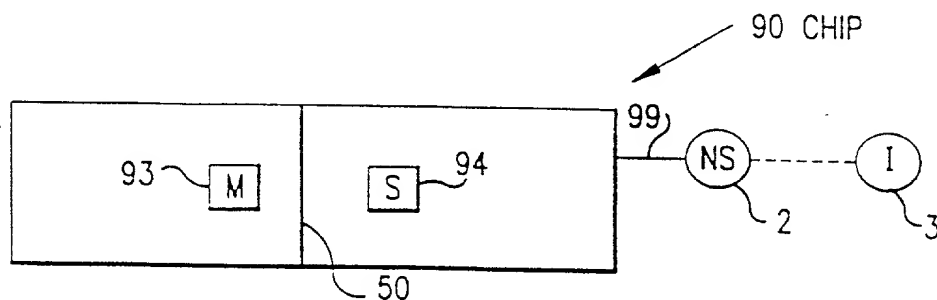


FIG. 10D

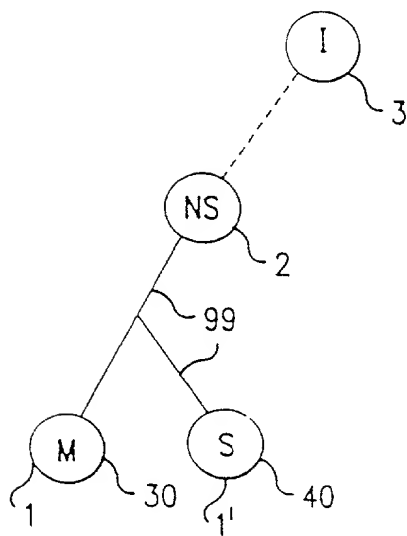


FIG. 10E

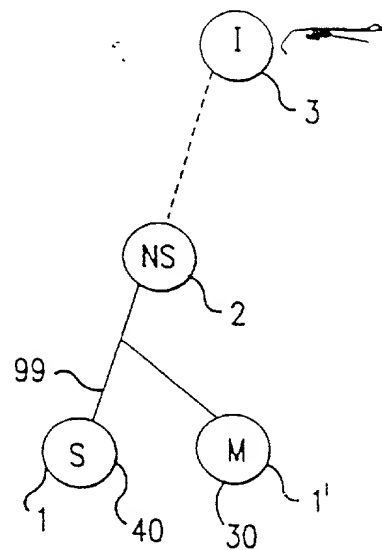


FIG. 10F

009260" DE 69960

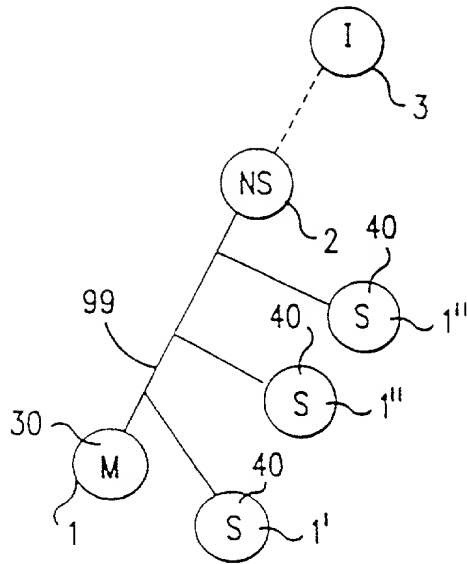


FIG. 10G

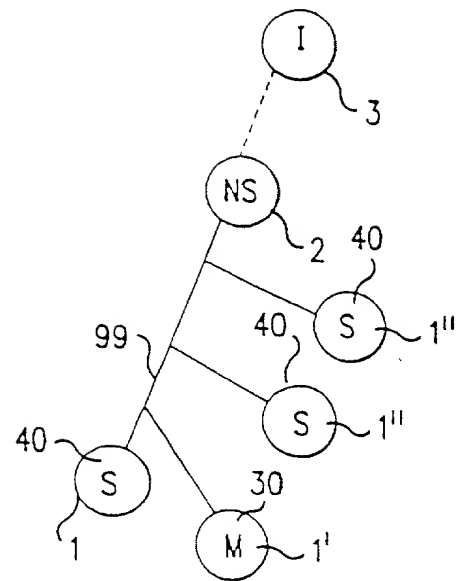


FIG. 10H

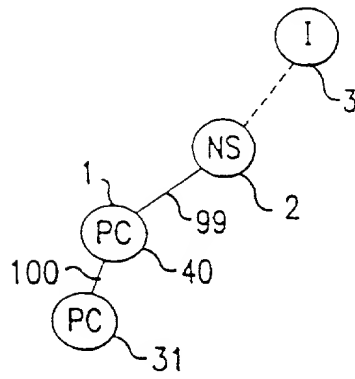


FIG. 10I

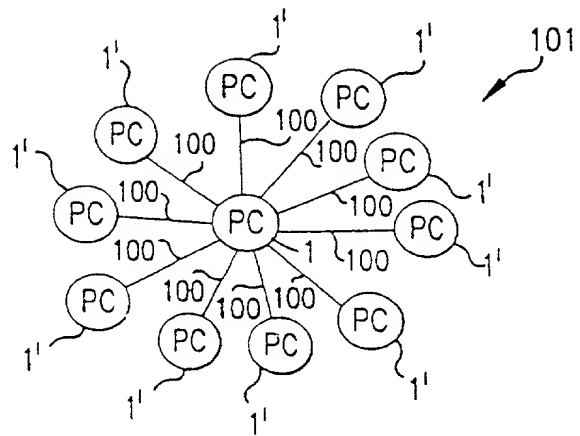
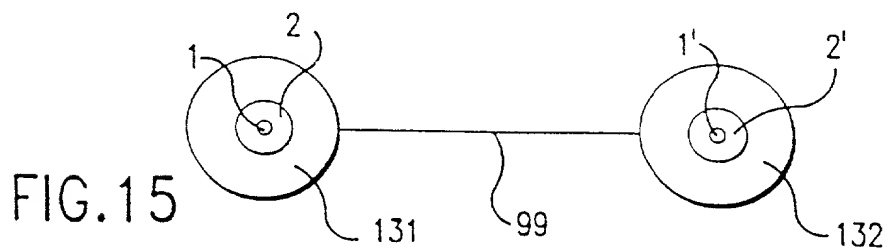
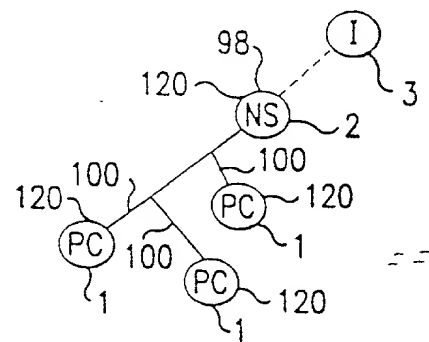
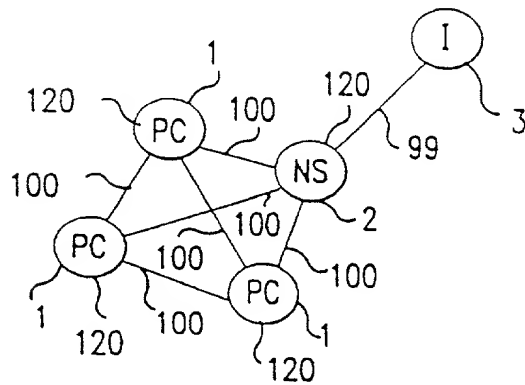
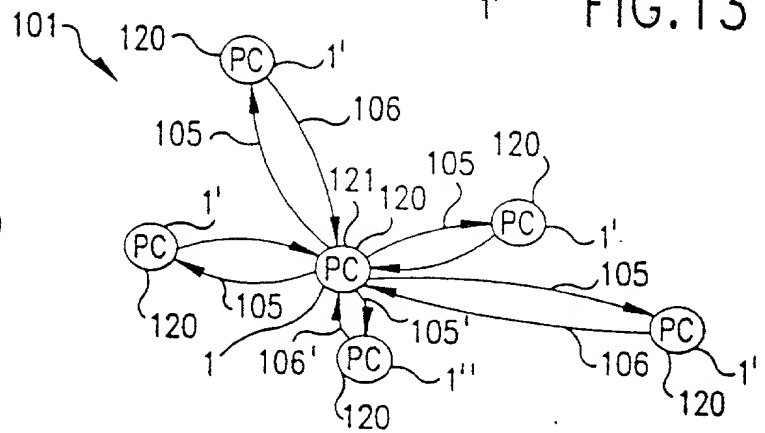
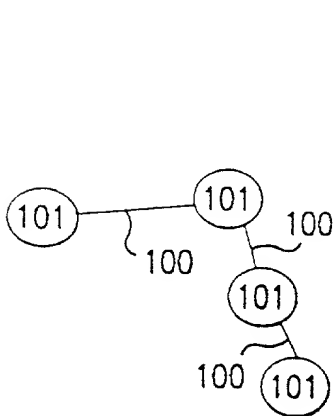
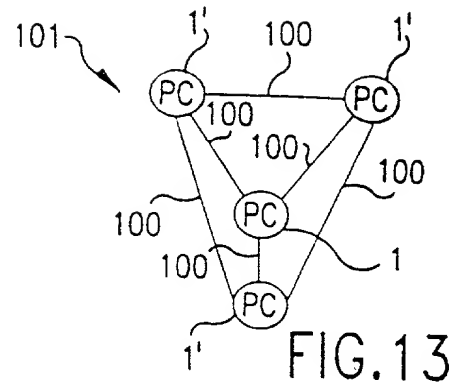
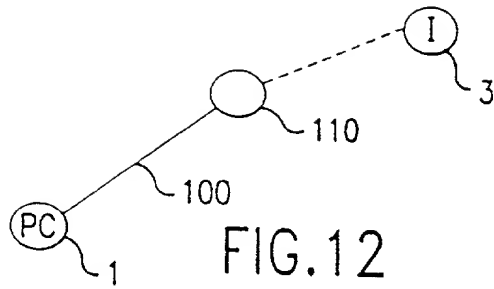


FIG. 11





**FOR UTILITY/DESIGN  
CIP/PCT NATIONAL/PLANT  
ORIGINAL/SUBSTITUTE/SUPPLEMENTAL  
DECLARATIONS**

**RULE 63 (37 C.F.R. 1.63)  
DECLARATION AND POWER OF ATTORNEY  
FOR PATENT APPLICATION  
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**PM & S  
FORM**

As a below named inventor, I hereby declare that my residence, post office address and citizenship are as stated below next to my name, and I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the INVENTION ENTITLED GLOBAL NETWORK COMPUTERS

the specification of which (CHECK applicable BOX(ES))  
☒ is attached hereto.  
 BOX(ES) ☐ was filed on \_\_\_\_\_ as U.S. Application No. \_\_\_\_\_  
☐ was filed as PCT International Application No. PCT/\_\_\_\_\_ on \_\_\_\_\_

and (if applicable to U.S. or PCT application) was amended on \_\_\_\_\_  
 I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose all information known to me to be material to patentability as defined in 37 C.F.R. 1.56. I hereby claim foreign priority benefits under 35 U.S.C. 119/365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate filed by me or my assignee disclosing the subject matter claimed in this application and having a filing date (1) before that of the application on which priority is claimed, or (2) if no priority claimed, before the filing date of this application:

PRIOR FOREIGN APPLICATION(S)		Date first Laid- open or Published	Date Patented or Granted	Priority Claimed Yes No
Number	Country	Day/MONTH/Year Filed		

I hereby claim domestic priority benefit under 35 U.S.C. 119/120/365 of the indicated United States applications listed below and PCT international applications listed above or below and, if this is a continuation-in-part (CIP) application, insofar as the subject matter disclosed and claimed in this application is in addition to that disclosed in such prior applications, I acknowledge the duty to disclose all information known to me to be material to patentability as defined in 37 C.F.R. 1.56 which became available between the filing date of each such prior application and the national or PCT international filing date of this application:

PRIOR U.S. PROVISIONAL, NONPROVISIONAL AND/OR PCT APPLICATION(S)		Status	Priority Claimed
Application No. (series code/serial no.)	Day/MONTH/Year Filed	pending, abandoned, patented	Yes No
60/031,855	29 NOV 1996	Expired	X
60/032,207	02 DEC 1996	Expired	X
60/033,871	20 DEC 1996	Expired	X
60/066,313	21 NOV 1997	Expired	X
60/066,415	24 NOV 1997	Expired	X
60/980,058	26 NOV 1997	Pending	X
PCT/US97/21812	28 NOV 1997	Pending	X
09/320,660	27 MAY 1999	Pending	X

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

And I hereby appoint Pillsbury Madison & Sutro LLP, Intellectual Property Group, 1100 New York Avenue, N.W., Ninth Floor, East Tower, Washington, D.C. 20005-3918, telephone number (202) 861-3000 (to whom all communications are to be directed), and the below-named persons (of the same address) individually and collectively my attorneys to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith and with the resulting patent, and I hereby authorize them to delete names/numbers below of persons no longer with their firm and to act and rely on instructions from and communicate directly with the person/assignee/attorney/firm/ organization who/which first sends/sent this case to them and by whom/which I hereby declare that I have consented after full disclosure to be represented unless/until I instruct the above firm and/or a below attorney in writing to the contrary.

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Carl G. Love	18781	Kendrew H. Colton	30368	Ruth N. Morduch	31044		
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Post Office Address		
(include Zip Code)		

(FOR ADDITIONAL INVENTORS, check box ☐ to attach PAT 116-2 same information for each re signature, name, date, citizenship, residence and address.)